

EXPEDITED PROCEDURE – EXAMINING GROUP 2826

S/N 09/691,004

PATENT

U 22 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al. Examiner: Johannes P Mondt
Serial No.: 09/691,004 Group Art Unit: 2826
Filed: October 18, 2000 Docket No.: 303.324US4
Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND
METHODS OF FABRICATION AND USE

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed May 17, 2004, please amend the application as follows:

Entered
W/RCE
9/84/04
C.Pat.